

Scheduling with Setup Considerations: An MIP Approach

Mohamed. K. Omar, Siew C. Teo and Yasothei Suppiah
Centre of Computer Aided Design and Knowledge Manufacturing (CCADKM)
Faculty of Engineering and Technology, Multimedia University
Malaysia

1. Introduction

Competitions and ever-changing customer requirements are the driven forces behind manufacturers to reevaluate their planning and scheduling methods and manufacturing systems. Customers' satisfaction in most cases can be measured by the ability of the manufacturing firms to provide goods with reasonably good prices, acceptable quality standard and deliver at the right time. Scheduling plays an important role in all of the important issues that are considered to measure customers' satisfaction. In recent years, there has been an increased interest in production planning problems in the multi product chemical and pharmaceutical industry. Multi product chemical plants use either a continuous production system or a batch production system. Batch process plants involve small amounts of a large variety of finished products, therefore are suitable for the production of small-volume, high-value added products. In such industry, products are often grouped into incompatible product families, where an intensive setup is incurred, whenever production changes from one product family to another.

A classical example of the multi product chemical plants is the manufacturing of resins. Typically, in the resin production environment, the planning and scheduling task starts by considering a set of orders where each order specifies the product and the amount to be manufactured as well as the promised due date. The most important task of the planner is the so-called batching of orders. Batching of orders is the process of transforming customers' product orders into sets of batches to be planned and subsequently assigned due date. This process is commonly practiced in the industry such as this, since a batch is frequently shared by several orders with the earliest one determining the batch due date. Moreover, while the planner is carrying out this task, his/her objective is to minimize as much as possible the setups between products that are generated from incompatible families. Therefore, in such manufacturing environment, setup activities cannot be disregarded and the production range is usually composed of a number of incompatible product families, in a way that no setup is required between production of two products belonging to the same family; long and expensive setup operations are required otherwise.

Scheduling is known as a decision-making process of allocating limited resources over time in order to perform a collection of tasks for the purpose of optimizing certain objectives functions (Baker 1974). Tasks can have difference in their priority levels, ready time, and

Source: Multiprocessor Scheduling: Theory and Applications, Book edited by Eugene Levner, ISBN 978-3-902613-02-8, pp.436, December 2007, Itech Education and Publishing, Vienna, Austria

process times. The objective function could be, for example, minimizing completion time, minimizing the number of tardy jobs, or adopting the (JIT) concepts and calls for minimization of earliness and tardiness. There are two issues associated with scheduling problems: how to allocate jobs on machines and how to sequence jobs on each machine. Therefore, the scheduler is mainly concerned with allocation decisions and sequencing decisions. On another issue, one must state at this stage that there is a difference between sequencing and scheduling. Sequencing corresponds to a permutation of the job set in which jobs are processed on a given machine. While scheduling is defined as an allocation of jobs within a more complicated setting of machines, which could allow for preemptions of jobs by other jobs that are released at a later point of time.

In the scheduling literature, setups have for long been considered negligible and hence ignored, or considered as part of the process time. But there are situations that call for treating the setups separately from the process time. In such cases, two problem types exist. In the first type, setups depend only on the job to be produced; hence, it is called *sequence-independent*. In the second type, setups depend on both the job to be processed and the immediate preceding job; hence it is called *sequence-dependent*.

This paper aims to explore the scheduling and sequencing problem confronted by planners in the multi product chemical plants that involve sequencing of jobs originated from incompatible families making it a situation that requires sequencing of jobs with sequence-dependent setup time. Our intension is to focus on these types of scheduling problems and suggest two mixed integer programming (MIP) formulations. The first formulation considers a single machine situation and aims to minimize total tardiness, while the second formulation attempts to minimize the sum of total earliness/tardiness for parallel machine situation.

This paper is organized as follows: Section 2 presents the literature review. Section 3 introduces a typical multi product chemical production environment. Section 4 presents problem description and formulation. We present our computational example in Section 5. Finally, we present our conclusions and remarks in Section 6.

2. Literature review

Enormous solutions have been proposed for machine scheduling problems, and we do not attempt to cover it all here. However, interested readers are referred to the reported reviews by Allahverdi et al. (1999), Yang and Liao (1999), Cheng et al. (2000), Potts and Kovalyov (2000) and Allahverdi et al. (in press). However, we will provide a brief review related to our work for total tardiness for single machine and the case of earliness/tardiness for parallel machines situation.

2.1 Single machine total tardiness problem

Tardiness is the positive lateness a job incurs if it is completed after its due date and the objective is to sequence the jobs to minimize total tardiness. In the weighted case, each job's tardiness is multiplied by a positive weight. The weighted tardiness problem in a single machine is NP-hard in the strong sense (Lenstra et al (1977)). Adding the characteristics of jobs originated from incompatible families increases the difficulty of the problem of minimizing the total weighted tardiness on a single machine. Many practical industrial situations require the explicit consideration of setups and the development of appropriate

scheduling tools. Among the reported cases, Pinedo (2002) describes a manufacturing plant making paper bags where setups are required when the type of bag changes. A similar situation was observed in the plastic industry by Das et al. (1995). The aluminium industry has a casting operation where setups, mainly affecting the holding furnaces are required between the castings of different alloys (see Gravel et al. (2000)).

Previous research done in the case of incompatible job families has been focused mostly on single batch machine problems. Fanti et al. (1996) focused on makespan as the performance measurement. Kemf et al. (1998) investigated a single machine having a second resource requirement, with a goal of minimizing makespan and total completion time. Dobson and Nambimodom (2001) considered the problem of minimizing the mean weighted flow time and provided an integer programming (IP) formulation. Mehta and Uzsoy (1998) presented a dynamic programming (DP) algorithm as well as heuristics that can provide near optimal solutions where the performance under analysis is total tardiness. Azizoglu and Webster (2001) describe a branch and bound procedure to minimize total weighted completion time with arbitrary job sizes. Their procedure returns optimal solutions to problems of up to 25 jobs. Most recently, Perez et al. (2005) developed and tested several heuristics to minimize the total weighted tardiness on single machines with incompatible job families. Their tests consistently show that the heuristics that uses Apparent Tardiness Cost (ATC) rule to form batches, combined with Decomposition heuristics (DH) to sequence jobs, perform better than others tested, except ATC combined with Dynamic Programming algorithms (DP). Their tests show that ATC-DH and ATC-DP results are close.

The literature is also not extensive either for single machine scheduling problems with sequence-dependent setups, where the objective is to meet delivery dates or to reduce tardiness. However, Lee et al. (1997) have proposed the Apparent Tardiness Cost with Setups (ATCS), a dispatching rule for minimizing weighted tardiness. Among other authors who have treated the problem, we find Rubin and Raagatz (1995) developed a genetic algorithm and local improvement method while Tan and Narasimhan (1997) used simulated annealing as a solution procedure. Tan et al. (2000) presented a comparison of four approaches and concludes, following a statistical analysis, that a local improvement method offers a better performance than simulating annealing, which in turn is better than branch-and-bound. In this comparison, the genetic algorithm had the worst performance.

2.2 Parallel machines with earliness/tardiness problem

Another scheduling approach that considers job earliness and tardiness penalties is motivated by the just-in-time concept (JIT). This approach requires only the necessary units to be provided with the necessary quantities, at the necessary times. Production of one extra unit is as bad as being one unit short. In today's manufacturing environments, many firms are required to develop schedules that complete each customer's order at, or near, its due date, and at the same time to ensure the cost-efficient running of the plant.

There are a large number of published research papers that consider scheduling problems, with both earliness and tardiness penalties. These include models with common due dates or distinct due dates, with common/symmetrical penalty functions as well as distinct job dependent penalty functions. Except for a few basic models, most of these scheduling problems have been shown to be NP-Hard. Readers are referred to the work of Webster [1997] and Chen [1997] for discussion, about the complexity boundaries of these problems. Readers interested in earliness-tardiness scheduling are referred to the survey conducted by

Baker and Scudder [1990] and the recent book by T'kindt and Billout [2000]. Readers especially interested in earliness and tardiness scheduling with setup considerations, are referred to the survey article by Allahverdi et al. [in press]. However, we summarize below some published works related to earliness and tardiness scheduling problems considered in this paper.

Kanet [1981] examined the earliness and tardiness problem, for a single machine, with equal penalties and unrestricted common due dates. A problem is considered unrestricted, when the due date is large enough not to constrain the scheduling problem. He introduced a polynomial time algorithm to solve the problem optimally. Hall [1986] extended Kanet's work and developed an algorithm that finds a set of optimal solutions for the problem based on some optimality conditions. Hall and Posner [1991] solved the weighted version of the problem with no setup times. Azizoglu and Webster [1997] introduced a branch-and-bound algorithm to solve the problem with setup times. Other researchers who worked on the same problems with a restricted (small) due date, included Bagchi et al. [1986], Szwarc [1989, 1996], Hall et al. [1991], Alidee and Dragan [1997] and Mondal and Sen [2001]. None of the previous papers consider sequence-dependent setup times.

The majority of the literature on earliness and tardiness scheduling deals with problems that consider single machine only. Problems with multiple machines have been investigated in only a handful of papers which includes among others, Emmons [1986], Cheng and Chen [1994], De et al. [1994], Li and Cheng [1994], Kramer and Lee [1994], Federgruen and Mosheiov [1996,1997], Adamopoulos and Pappis [1998] and Chen and Powell [1999]. To the best of our knowledge, there have been very few publications that propose a mixed integer programming solution for parallel machines that consider setup for the earliness and tardiness scheduling problem. Balakrishnan et al. [1999] considers the problem of scheduling jobs on several uniform parallel machines and presented a mixed integer programming formulation. However, their reported experiments show that their approach cannot solve a problem with more than 10 jobs. More recently, Zhu and Heady [2000] proposed a mixed integer programming formulation for minimizing job earliness and tardiness scheduling problem for a non-uniform parallel machine and setup considerations. However, their reported experiments show that their approach cannot solve a problem with more than 10 jobs. Furthermore, their reported formulation suffers from a serious error making their reported job/machine assignment and sequential job orders questionable. And the work of Omar and Teo (2006) whom they corrected Zhu and Heady (2000) and proposed an improved MIP formulation for minimizing the sum of earliness/tardiness in identical parallel machine. Their tests show that their proposed formulation can provide optimal solution for 18 jobs originated from 4 incompatible families.

3. Production environment

A resin manufacturing company in South East Asia will be used to illustrate the production environment. The plant has two production lines and the major types of production reactions include Alklylation, Acylation and Aminotion, leading to the production of over 100 finished products. Figure 1 show the structure of the most active 20 products which are generated from 5 incompatible families.

The plant operates on three shifts, and each production year has 358 days. Working capacity is around 742 tons and 663 tons for line one and line two respectively. The operation in each production line is a reaction process, where the chemical reaction takes

place in a reactor; mixing where chemicals are mixed in a thinning tank; filtering, where purities are controlled to meet customer's request; and packaging. Reaction is the bottleneck operation, hence the working capacities estimated, are based on the reaction process. Demand of finished products is considered to be high and therefore, all products cannot be satisfied from production runs, since some of the available capacity is consumed for setups. The workforce involved on the production is very limited and each shift requires 7 persons to run the process and the company does not practice workforce variation policies.

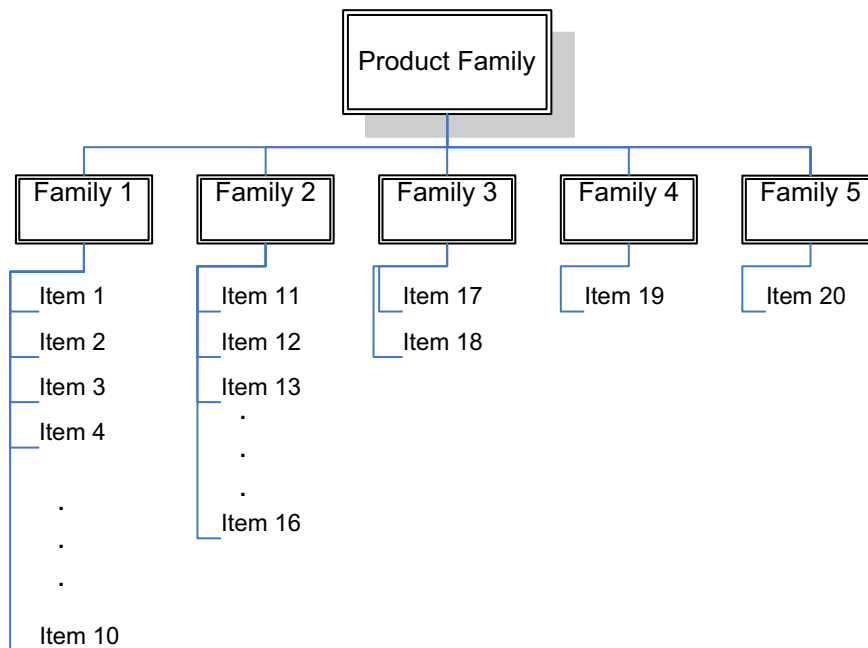


Figure 1. Distribution of Product families for the most active products

When the demand estimates for the next year are ready, the marketing division passes these estimates to the production division to prepare the operational budget for the next year. The order batching process starts when the production planner receives customers' orders with due dates. The ultimate objective of this process is to meet the customers due dates and minimize setup activities. Interested readers are referred to the work reported by Omar and Teo (2007) for detailed solution for the planning and scheduling problem described in this section.

4. Problem formulation

In the production environment described above, the scheduling and sequencing problem can be formulated in various ways. We will present two different formulations that reflect some management policies that the company might wish to implement. First, the management might wish to implement a product/production line dedication policy, and in that case, the two production lines will be treated as a two separate single production lines,

or in another world, two separate single machine situation. For this case, we will provide an MIP modeling approach that aims to minimize total tardiness. In the second case, the company may consider examining the idea which assumes that that all products can be at any instant of time produced in any of the production lines. In such a case, we will provide an MIP modeling approach that treats this situation as identical parallel machines and aims to minimize the total earliness and tardiness.

It is worth noting that MIP codes have a weakness when confronted with real life industrial scheduling and sequencing problems that involve hundreds of products, since the computational time will increase exponentially as the number of integer variables increase. Consequently, the decision maker may not be able to obtain results in real time to be of any use for implementation purposes. However, MIP codes are beneficial to researchers for testing the performance of their developed heuristics, which are normally developed for industrial application and tested against other heuristics, a dangerous procedure practiced by researchers (see Ovacik and Uzsoy (1994)).

4.1. Single machine problem formulation

Notations

Parameters

m = number of families.

n_i = number of jobs in family i .

n = total number of jobs

d_{ij} = due date of j^{th} job in family i .

p_{ij} = processing time of j^{th} job in family i .

s_i = setup time of family i .

Decision Variables:

$$X_{ijk} = \begin{cases} 1 & \text{if job } j \text{ from family } i \text{ is placed in position } k \\ 0 & \text{otherwise} \end{cases}$$

$$Y_{ik} = \begin{cases} 1 & \text{if setup } s_i \text{ is needed before a job at position } k \\ 0 & \text{otherwise} \end{cases}$$

C_k = completion time of the job at position k .

T_{ijk} = tardiness of the j^{th} job in family i at position k

Formulation

$$\text{Min } \sum_{i=1}^m \sum_{j=1}^{n_i} \sum_{k=1}^n T_{ijk} \quad (1)$$

Subject to:

$$\sum_{i=1}^m \sum_{j=1}^{n_i} X_{ijk} = 1 \quad k = 1, 2, \dots, n \quad (2)$$

$$\sum_{k=1}^n X_{ijk} = 1 \quad i = 1, 2, \dots, m; j = 1, 2, \dots, n_i \quad (3)$$

$$\sum_{j=1}^{n_i} X_{ijk} + \sum_{j=1}^{n_i} \sum_{\substack{p \in \{1,2,\dots,m\} \\ -\{i\}}} X_{pj(k-1)} - Y_{ik} \leq 1 \quad k = 1, 2, \dots, n; i = 1, 2, \dots, m \quad (4)$$

$$C_1 = \sum_{i=1}^m \sum_{j=1}^{n_i} p_{ij} X_{ij1} \quad (5)$$

$$C_k = C_{k-1} + \sum_{i=1}^m s_i Y_{ik} + \sum_{i=1}^m \sum_{j=1}^{n_i} p_{ij} X_{ijk} \quad k = 2, 3, \dots, n \quad (6)$$

$$C_k - \sum_{i=1}^m \sum_{j=1}^{n_i} d_{ij} X_{ijk} \leq \sum_{i=1}^m \sum_{j=1}^{n_i} T_{ijk} X_{ijk} \quad k = 1, 2, \dots, n \quad (7)$$

$$C_k \geq 0 \quad k = 1, 2, \dots, n \quad (8)$$

$$T_{ijk} \geq 0 \quad i = 1, 2, \dots, m; j = 1, 2, \dots, n_i; k = 1, 2, \dots, n \quad (9)$$

In the above formulation, equation (1) represents the objective function, which is to minimize total tardiness. Equations (2) and (3) state that each position can be occupied by only 1 job and each job can be processed only once. Equation (4) checks whether or not the preceding job and the following job are from the same family. If so, there is no setup time between them. Otherwise, a family setup time of the job in position k exists. Equation (5) states the completion time of the job in the first position. Equation (6) calculates the completion time from the second position to the last position of the sequence. Equation (7) determines the tardiness values for all positions. Equations (8) and (9) give the non-negativity constraints.

4.2. Parallel machines problem formulation

Notations

Parameters:

m = number of families.

r = number of production lines.

n = total number of jobs.

f_j = family of job j , $f_j = 1, 2, \dots, m$

d_j = due date for job j .

p_{jl} = processing time of job j at production line l .

e_j = earliness penalty for job j .

t_j = tardiness penalty per period for job j .

s_{jk} = setup time from family of job j to family of job k .

$$G_{jk} = \begin{cases} s_{jk} & \text{if } f_j \neq f_k \\ 0 & \text{if } f_j = f_k \end{cases}$$

M = A large positive number.

Decision Variables:

E_j = earliness for job j .

T_j = tardiness for job j .

C_j = completion time of job j .

$\alpha_{jl} = \begin{cases} 1 & \text{if job } j \text{ is the first processed in line } l. \\ 0 & \text{otherwise} \end{cases}$

$\theta_{jk} = \begin{cases} 1 & \text{if job } k \text{ has been scheduled right after job } j. \\ 0 & \text{otherwise} \end{cases}$

β_{jl} : Continuous variable restricted to the range $[0, 1]$, denoting that job j has been scheduled in line l but not in first place.

Formulation

$$\text{Min} \quad \sum_{j=1}^n (e_j E_j + t_j T_j) \quad (10)$$

Subjects to:

$$\sum_{l=1}^r (\alpha_{jl} + \beta_{jl}) = 1, \quad j = 1, 2, \dots, n \quad (11)$$

$$\alpha_{jl} + \beta_{jl} \leq \beta_{kl} + 1 - \theta_{jk}, \quad j = 1, 2, \dots, n; k = 1, 2, \dots, n; l = 1, 2, \dots, r \quad (12)$$

$$\sum_{j=1}^n \alpha_{jl} \leq 1, \quad l = 1, 2, \dots, r \quad (13)$$

$$\sum_{l=1}^r \alpha_{jl} + \sum_{k=1}^n \theta_{kj} = 1, \quad j = 1, 2, \dots, n \quad (14)$$

$$\sum_{k=1}^n \theta_{jk} \leq 1, \quad j = 1, 2, \dots, n \quad (15)$$

$$C_k \geq C_j + G_{jk} + \sum_{l=1}^r p_{kl} \beta_{kl} + M \theta_{jk} - M, \quad j = 1, 2, \dots, n; k = 1, 2, \dots, n \quad (16)$$

$$C_j \geq \sum_{l=1}^r p_{jl} (\alpha_{jl} + \beta_{jl}), \quad j = 1, 2, \dots, n \quad (17)$$

$$C_j + E_j - T_j = d_j, \quad j = 1, 2, \dots, n \quad (18)$$

$$d_j \theta_{jk} \leq d_k \theta_{jk}, \quad j = 1, 2, \dots, n; k = 1, 2, \dots, n \quad (19)$$

$$C_j, E_j, T_j \geq 0, \quad j = 1, 2, \dots, n \quad (20)$$

In the above formulation, equation (10) represents the objective function, which is to minimize the weighted sum of earliness-tardiness. Equation (11) states that each job must be assigned to one production line. Equation (12) enforces both the job and its direct successors in the processing sequence to be manufactured on the same line. Equation (13) states that each job, if first, can only be processed first on one line. Equation (14) enforces that a job is

either the first to be processed, or succeed another in the processing sequence. Equation (15) ensures that every job should at most be directly succeeded by another job in the processing sequence, unless it is last in the sequence. Equation (16) ensures that the processing start time for a job can never be lower than the completion time of its direct predecessor job in the processing sequence. Equation (17) states that completion time of a job must be later or equal to its processing time. Equation (18) measures the degree to which each job is tardy or early. Equation (19) states that the due date of a job must be the same or earlier than its direct successor job. Equation (20) is the non-negativity constraint.

5. Computational example

The models are illustrated using the data shown in Tables 1 and 2. The data presented in Table 1 is for a scheduling and sequencing problem that consists of 10 jobs that can be originated from 2, 3 or 4 incompatible families. As it could be seen in Table 1, the setup time required when the production runs change from one family to another is fixed. On the other hand, Table 2 is used to create variable setup times among the different product families. It is worth noting that in our computations for parallel machines situation, earliness penalty was calculated using the value of $(J + 1)^{-1}$ whereas the tardiness penalty was kept to be equal to one.

10 jobs originated from 2 incompatible families										
J	1	2	3	4	5	6	7	8	9	10
F	1	1	1	1	1	2	2	2	2	2
P	3	8	9	5	2	6	11	4	10	7
DD	11	18	16	17	27	19	15	12	21	26
Setup	1	1	1	1	1	1	1	1	1	1
10 jobs originated from 3 incompatible families										
J	1	2	3	4	5	6	7	8	9	10
F	1	1	1	1	1	2	2	3	3	3
P	3	8	9	5	2	6	11	4	10	7
DD	11	18	16	17	27	19	15	12	21	26
Setup	1	1	1	1	1	1	1	1	1	1
10 jobs originated from 4 incompatible families										
J	1	2	3	4	5	6	7	8	9	10
F	1	1	1	2	2	3	3	3	4	4
P	3	8	9	5	2	6	11	4	10	7
DD	11	18	16	17	27	19	15	12	21	26
Setup	1	1	1	1	1	1	1	1	1	1
J=Job, F= Family. P=process time in hours. DD=Due date in hours. Setup in hours										

Table 1. Ten jobs originated from different incompatible families with constant setup time

In this study, the MIP models were developed using OPL Studio version 3.6 and solved using CPLEX version 8. Models were executed with Pentium IV 2.80Hz. processor, while Microsoft Excel is used to export and import data and solution. The data required by the developed MIP models, and to be used for illustrative purposes is presented in Tables 1 and 2.

	F1	F2	F3	F4
F1	0	2	1	2
F2	1	0	2	3
F3	2	1	0	1
F4	3	2	1	0

Table 2. Families setup time matrix

No. of jobs	No. of families	Sequence	Total tardiness	No. of setups
Single machine with constant setup time				
10	2	1→4→8→6→10→5→2→3→9→7	141	3
10	3	8→1→4→2→5→6→10→9→3→7	150	4
10	4	1→8→6→4→5→10→9→2→3→7	154	5
Single machine with variant setup time				
10	2	8→6→1→4→2→5→3→10→9→7	148	2
10	3	4→1→8→6→5→2→3→10→9→7	153	5
10	4	1→8→6→4→5→2→3→10→9→7	157	5
Parallel machines with constant setup time				
10	2	L1:8→7→6→10→5	31.88	2
		L2:1→3→4→2→9		
10	3	L1:1→3→4→2→9	37.80	4
		L2:8→7→6→10→5		
10	4	L1:1→3→4→2→9	38.81	5
		L2:8→7→6→10→5		
Parallel machines with variant setup time				
10	2	L1:1→3→4→2→5	32.80	-
		L2:8→7→6→9→10		
10	3	L1:1→3→4→2→9	39.9	4
		L2:8→7→6→10→5		
10	4	L1:8→7→6→10→5	43.81	5
		L2:1→3→4→2→9		

Table 3 Summary of the computational results

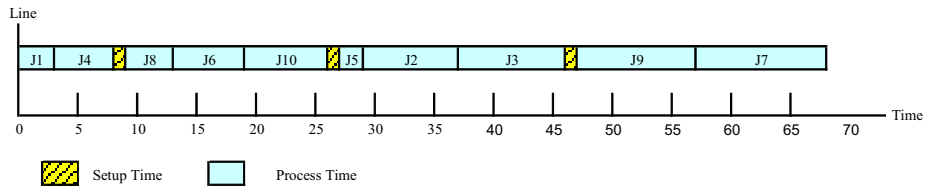


Figure 2. for 10J 2 F single machine fixed setup times

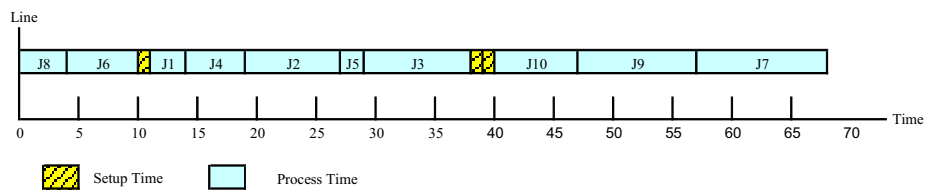


Figure 3. for 10J 2 F single machine variable setup times

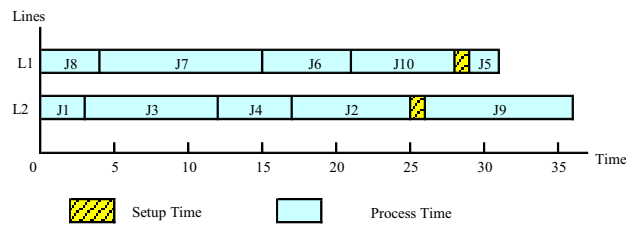


Figure 4. for 10J 2 F parallel machines fixed setup times

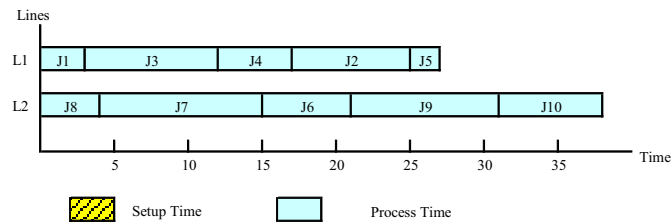


Figure 5. for 10J 2 F parallel machines variable setup times

6. Concluding remarks

The results of the performance of the developed MIP for single and parallel machines are summarized in Table 3 and the sample of the results is presented in Gantt chart is shown in Figures 2-5. Examining the results presented in Table 3 reveals that for all cases tested, total tardiness and number of setups increases as the number of incompatible families involved

in the scheduling activities increases. As it is expected, when more resources are involved in the scheduling activities, the total tardiness will decrease, when an additional machine is added, tardiness improved almost 5 times

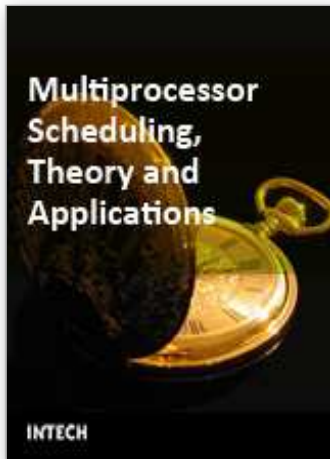
As a summary, in this research we presented the characteristics of an industrial environment where products (jobs) that originated from incompatible families are required to be scheduled and sequenced to meet some predetermined due dates. We presented two MIP formulations to solve such scheduling and sequencing requirements. The first MIP formulation aims to minimize the total tardiness while the second MIP formulation adopts the just-in-time concept and calls for minimizing the sum of earliness and tardiness for parallel machines. Moreover, we presented computational examples that consider fixed and variable setup times when the production runs changes from one product family to another. It is worth noting that with MIP models, computational time grows in an almost exponential manner as the problem size is increased. This known fact is considered as a major drawback for using MIP in real industrial application, none the less, MIP models are the only way to check optimality of heuristics solutions employed to solve industrial size scheduling problems.

7. References

- Adamopoulos, G.I., Pappis, C.P. (1998) Scheduling Under Common Due Date on Parallel Unrelated Machines, *European Journal of Operational Research*, 105: 494-501.
- Allahverdi, A., Gupta JND and Aldowaisan T (1999). A Review of Scheduling Research Involving Setup Considerations. *Omega* 27: 219-239.
- Allahverdi, A., Ng CT., Cheng TCE, Mikhail Y (2006). A Survey of Scheduling Problems with Setup Times or Costs. *European Journal of Operational Research*. In Press.
- Azizoglu, M., Webster, S. (1997) Scheduling Job Families About an Unrestricted Common Due Date on a Single Machine, *International Journal of Production Research*, 35 (5): 1321-1330.
- Azizoglu, M., Webster, S. (2001). Scheduling a Batch Process Machine with Incompatible Job Families. *Computers and Industrial Engineering* 39: 325-335.
- Bagchi, U., Sullivan, R., Chang, Y-L (1986) Minimizing Mean Absolute Deviation of Completion Times About a common due date, *Naval Research Logistics*, 33: 227-240.
- B. Alidee, B., Dragan, I.. (1997) A Note on Minimizing the Weighted Sum of Tardy and Early Completion Penalties in a Single Machine: a Case of Small Common Due Date, *European Journal of Operation Research*, 96: 559-563.
- Baker, KR. (1974). Introduction to Sequencing and Scheduling, John Wiley, New York.
- Baker, K.R., Scudder, G. (1990) Sequencing with Earliness and Tardiness Penalties: a Review, *Operation Research*, 38: 22-36.
- Balakrishnan, N., Kanet, J.J., Sridharan, S.V. (1999) Early/Tardy Scheduling with Sequence Dependant Setups on Uniform Parallel Machines. *Computers & Operations Research*, 26: 127-141.
- Chen, Z. L. (1997) Scheduling with Batch Setup and Earliness and Tardiness Penalties, *European Journal of Operational Research*, 97 : 18-37.
- Chen, Z.L., Powell, W.B. (1999) Solving Parallel Machine Scheduling Problems by Column Generation, *INFOMS Journal on Computing*, 11 : 78-94.
- Cheng, T.C.E. Chen, Z.L. (1994) Parallel-Machine Scheduling with Earliness and Tardiness penalties, *Journal of Operation Research Society*, 45: 685-695.

- Cheng, T.C.E., Kovalyov, M. (2000). Parallel Machine Batching and Scheduling with Deadlines. *Journal of Scheduling* 3, 109-123.
- De, P., Ghosh, J.B., Wells, C.E. (1994) Due-Date Assignment and Early/Tardy Completion Costs, *Naval Research Logistics*, 41: 17-31.
- Dobson, G. and Nambimadom, R.S. (2001). The Batch Loading and Scheduling Problem. *Operations Research* 49(1): 52-65.
- Emmons, H. (1987), Scheduling to a Common Due Date on Parallel Uniform Processors, *Naval Research logistics*, 41: 17-32.
- Fanti, M.P., Maione, B., Piscitelli, G., Turchiano, B. (1996). Heuristic Scheduling of Jobs on a Multi-Product Batch Processing Machine. *International Journal of Production Research* 34(8): 3263 – 3286.
- Federgruen, A. Mosheiov, G. (1996) Heuristics for Multi- Machine Scheduling Problems with Earliness and Tardiness Costs, *Management Science* 42: 1544-1556.
- Federgruen, A. Mosheiov, G. (1997) Heuristics for Multi- Machine Min-Max Scheduling Problems with General Earliness and Tardiness Costs, *Naval Research Logistics*, 44: 287-299.
- Gravel, M., Price, W.I. Gagnec, C. (2000). Scheduling Jobs in an Alcan Aluminum Factory Using a Genetic Algorithm. *International Journal of Production Research* 38: 3031-3041.
- Hall, N.G. (1986) Single-and Multiple- Processor Models for Minimizing Completion Time Variance, *Naval Research Logistics*, 33: 49-54.
- Hall, N.G., Kubiak, W., Sethi, S.P. (1991) Earliness-Tardiness Problems, II: Deviation of Completion Times About Restrictive Common Due Date, *Operation Research*, 39 (5): 847-856.
- Hall, N.G., Posner, M. E. (1991) Earliness-Tardiness Scheduling Problems I, Weighted Deviation of Completion Times About a Common Due Date, *Operation Research*, 39 (5): 836-846.
- Kanet, J.J. (1981) Minimizing the Average Deviation of Job Completion Times About a Common due Date, *Naval Research Logistics*, 28: 643-651.
- Kempf, K.G., Uzsoy, R., Wang, C.S. (1998). Scheduling a Single Batch Processing Machine with Secondary Resource Constraints. *Journal of Manufacturing Systems* 17(1): 37-51.
- Kramer, F.J., Lee, C.Y. (1994) Due window scheduling for parallel machines, *Mathematical and Computer Modeling*, 20 : 69-89.
- Lee, Y.H., Bhaskaran, K., Pinedo, M. (1997). A Heuristic to Minimize the Total Weighted Tardiness with Sequence-Dependent Setups. *IIE Transaction* 29: 45-52.
- Lenstra, J.K., Rinnooy Kan, A.H.G., Brucker P (1997). Complexity of Machine Scheduling Problems. *Annals of Decision Mathematics* 1:342-362.
- Li, C.L., Cheng, T.C.E. (1994) The Parallel Machine Min-Max Weighted Absolute Lateness Scheduling Problem, *Naval Research Logistics*, 41: 33-46.
- Mehta, S.V., Uzsoy, R. (1998). Minimizing Total Tardiness on a Batch Processing Machine with Incompatible Job Families. *IEE Transaction* 30: 165-178.
- Mondal, S.A., Sen, A.K. (2001) Single Machine Weighted Earliness-Tardiness Penalty Problem with Common Due Date, *Computers and Operations Research*, 28 (7): 649-669.
- Omar, M. K., Teo, S. C. (2007). Hierarchical Production Planning and Scheduling in a Multi-Product, Batch Process Environment. *International Journal of Production Research*, 55 (5):1029-1047.

- Omar, M. K., Teo, S. C. (2006). Minimizing the Sum of Earliness/Tardiness in Identical Parallel Machines Schedule with Incompatible Job Families: An Improved MIP Approach. *Applied Mathematics and Computations*, 181:1008-1017.
- Perez, I.C., Fowler, J. W., Carlyle, W. M. (2005). Minimizing Total Weighted Tardiness on a Single Batch Process Machine with Incompatible Job Families. *Computers and Operations Research*, 32, 327-341.
- Pinedo, M. (2002). *Scheduling Theory, Algorithms and Systems*. (2nd. ed.), Prentice Hall, New Jersey.
- Potts, C. N., Kovalyov, M., (2000). Scheduling with Batch: A review. *European Journal of Operational Research* 120,228-349.
- Rubin, PA., Ragatz, GL. (1995). Scheduling in a Sequence Dependent Setup Environment with Genetic Search. *Computers and Operation Research* 22: 85-99.
- Szwarc, W. (1989) Single Machine Scheduling to Minimize Absolute Deviation of Completion Times From a Common Due Date, *Naval Research Logistics*, 36: 663-673.
- Szwarc, W. (1996). The Weighted Common Due Date Single Machine Scheduling Problem Revisited, *Computers and Operations Research*, 23 (3): 255-262.
- Tan, KC., Narasimhan, R. (1997). Minimizing Tardiness on a Single Processor with Sequence-Dependent Setup Times: a Simulated Annealing Approach. *Omega* 25: 619-634.
- Tan, KC, Narasimhan R, Rubin, PA., Ragatz GL (2000). A Comparison of Four Methods for Minimizing Total Tardiness on a Single Processor with Sequence-Dependent Setup Times. *Omega* 28: 313-326.
- T'kindt, V., Billaut, J.C (2000) *Scheduling: Theory, Models and Algorithms*, Springer, Berlin.
- Yang, WH., Liao, CJ. (1999). Survey of Scheduling Research Involving Setup Times. *International Journal of Systems Science* 30: 143-155.
- Webster, S.T. (1997). The Complexity of Scheduling Job Families about a Common Due Date, *Operation Research Letters*, 20: 65-74.
- Zhu, Z. Heady, R.B. (2000) Minimizing the Sum of Earliness/Tardiness in Multi-Machine Scheduling: a Mixed Integer programming Approach. *Computer & Industry Engineering*, 38: 297-305.



Multiprocessor Scheduling, Theory and Applications

Edited by Eugene Levner

ISBN 978-3-902613-02-8

Hard cover, 436 pages

Publisher I-Tech Education and Publishing

Published online 01, December, 2007

Published in print edition December, 2007

A major goal of the book is to continue a good tradition - to bring together reputable researchers from different countries in order to provide a comprehensive coverage of advanced and modern topics in scheduling not yet reflected by other books. The virtual consortium of the authors has been created by using electronic exchanges; it comprises 50 authors from 18 different countries who have submitted 23 contributions to this collective product. In this sense, the volume can be added to a bookshelf with similar collective publications in scheduling, started by Coffman (1976) and successfully continued by Chretienne et al. (1995), Gutin and Punnen (2002), and Leung (2004). This volume contains four major parts that cover the following directions: the state of the art in theory and algorithms for classical and non-standard scheduling problems; new exact optimization algorithms, approximation algorithms with performance guarantees, heuristics and metaheuristics; novel models and approaches to scheduling; and, last but not least, several real-life applications and case studies.

How to reference

In order to correctly reference this scholarly work, feel free to copy and paste the following:

Mohamed. K. Omar, Siew C. Teo and Yasothei Suppiah (2007). Scheduling with Setup Considerations: An MIP Approach, Multiprocessor Scheduling, Theory and Applications, Eugene Levner (Ed.), ISBN: 978-3-902613-02-8, InTech, Available from:

http://www.intechopen.com/books/multiprocessor_scheduling_theory_and_applications/scheduling_with_setup_considerations__an_mip_approach

INTECH
open science | open minds

InTech Europe

University Campus STeP Ri
Slavka Krautzeka 83/A
51000 Rijeka, Croatia
Phone: +385 (51) 770 447
Fax: +385 (51) 686 166
www.intechopen.com

InTech China

Unit 405, Office Block, Hotel Equatorial Shanghai
No.65, Yan An Road (West), Shanghai, 200040, China
中国上海市延安西路65号上海国际贵都大饭店办公楼405单元
Phone: +86-21-62489820
Fax: +86-21-62489821

© 2007 The Author(s). Licensee IntechOpen. This chapter is distributed under the terms of the [Creative Commons Attribution-NonCommercial-ShareAlike-3.0 License](#), which permits use, distribution and reproduction for non-commercial purposes, provided the original is properly cited and derivative works building on this content are distributed under the same license.